

RITES LIMITED
(A Govt. of India Enterprise)
RITES Bhawan, Plot No. 1, Sector – 29, Gurgaon – 122001



Recruitment of GET on regular basis

The number of vacancies for recruitment of Graduate Engineer Trainees (GETs) – 2019 in S&T Discipline are as under:

VC No.	Post	Number of vacancies					
		UR	EWS	OBC	SC	ST	Total
11/19	Graduate Engineer Trainee (Signal & Telecommunications)	3	1	2	2	-	8

*1 Post reserved for PWD on horizontal basis

Candidates have been shortlisted for Interview on the basis of GATE Marks (Out of 100) only as entered by them in the RITES online Application Form.

Cut – off GATE Marks (Out of 100) for the purpose of shortlisting for Interview are as under:

Category	UR	EWS	OBC	SC	ST	PWD
Cut – off	72	56	63.33	50.33	-	29.67

Candidates with incomplete Application Form or form without GATE Details have not been considered.

In case of any queries/ discrepancies related to shortlisting, candidates may send an e-mail on rectt@rites.com along with the following details

- i. VC No.
- ii. REGISTRATION/ROLL NO.
- iii. NAME OF CANDIDATE IN FULL AND IN BLOCK LETTERS.
- iv. Valid email address as given in the application

Queries can be sent till 5:00 PM 16.08.2019 only after which no queries shall be entertained.